

OSMM&N File No. 251023US2S

✓ Serial No. 10/807,454

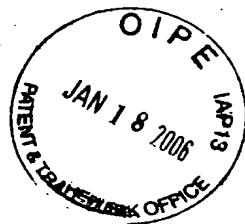
In the matter of the Application of: Yoshihisa IWATA et al.

For: SEMICONDUCTOR MEMORY DEVICE HAVING MEMORY CELLS
INCLUDING FERROMAGNETIC FILMS AND CONTROL METHOD
THEREOF

Due Date: January 25, 2006

The following has been received in the U.S. Patent Office on the date stamped hereon:

<input type="checkbox"/> Credit Card Form for <u>\$180.00</u>	<input type="checkbox"/> Dep. Acct. Order Form
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> PTO-1449
<input type="checkbox"/> Cited References (3)	
<input type="checkbox"/> Japanese Office Action with English Translation	



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Linked to OPTMS	
DATE	CASE ID



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yoshihisa IWATA et al.

SERIAL NO: 10/807,454

GAU: 2827

ALLOWED: December 6, 2005

FILED: March 24, 2004

EXAMINER: PHAN, T. Q.

FOR: SEMICONDUCTOR MEMORY DEVICE HAVING MEMORY CELLS INCLUDING FERROMAGNETIC FILMS
AND CONTROL METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449, which were cited in the attached Japanese Office Action. An English translation of the Japanese Office Action and copies of the listed references are attached.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

COPY

Customer Number

22850

Tel. (703) 413-3000

Fax. (703) 413-2220

(OSMMN 05/03)

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Eckhard H. Kuesters

Registration No. 28,870

Remus F. Fetea, Ph.D.

Limited Recognition No. L0037

Linked to OPTIVIS

DATE

CASE ID

JAN 31 2006

SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 251023US2S	PATENT & TRADEMARK OFFICE U.S. DEPARTMENT OF COMMERCE			SERIAL NO. 10/807,454
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Yoshihisa IWATA et al.						
		ALLOWED DATE December 6, 2005	FILING DATE March 24, 2004	GROUP 2827				
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
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	AJ							
	AK							
	AL							
	AM							
	AN							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION			
	AO	2003-173672	06/20/2003	JAPAN	YES	NO		
	AP	2003-257175	09/12/2003	JAPAN				
	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
	AW	Takeshi HONDA et al., "MRAM-Writing Circuitry to Compensate for Thermal-Variation of Magnetization-Reversal Current", 2002 Symposium On VLSI Circuits Digest of Technical Papers, IEEE, June 2002, pp. 156-157.						
	AX							
	AY							
	AZ					<input type="checkbox"/> Additional References sheet(s) attached		
Examiner					Date Considered			

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.